

Abstract

Apparatus and method for executing a Viterbi algorithm

5 The apparatus has a number of initial state registers, at least one transition register and an adder/subtracter network. Furthermore, a number of evaluation units and a selection unit are provided, by means of which the apparatus can be switched between a first operating mode and a second operating mode.

0 Different evaluation units can be selected by the selection unit depending on the selected operating mode.

Figure 1